

# ECS-SRIA 2021 & 2022 OVERVIEW

5th ECSEL Italy Day and Brokerage Event  
26 January 2022, online meeting

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# The ECS-SRIA

The ECS Strategic Research and Innovation Agenda (ECS-SRIA):

- describes the major challenges and priorities,
- and the necessary R&D&I efforts to tackle them,
- in the area of the electronics components and systems, and systems of systems
- spanning the entire ECS value chain, from foundational and cross-sectional technologies to application domains.

Joint effort of the 3 Industry Associations

- AENEAS, EPOSS and Inside-IA,
- 2021: with a core team of 12 members, 53 chapter leaders,
- 2022: with a core team of 9 members, 57 chapter leaders,
- and the involvement of more than 300 experts from the ECS community.

Funding-programme agnostic, open and living document looking 15 years ahead.

The ECS-SRIA is the reference document for:

- KDT (previously ECSEL), the KDT JU adopts the ECS-SRIA as the KDT-SRIA,
- EUREKA Clusters (e.g. Xecs).



Aeneas



EPOSS  
European Technology Platform  
on Smart Systems Integration



Inside  
Industry Association

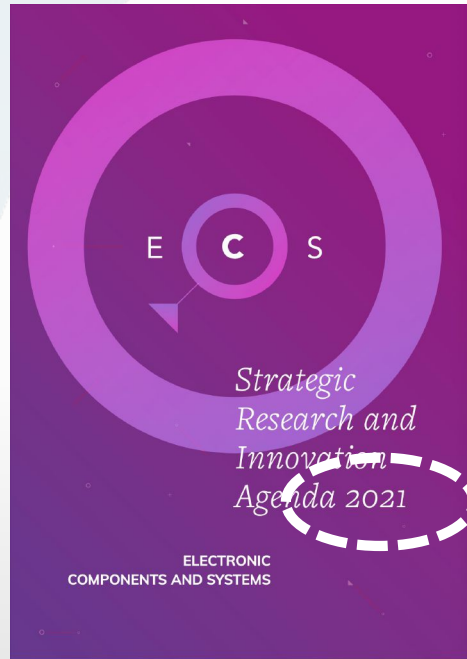
# ECS-SRIA 2021 vs 2022

- Edited in 2020
- Published in January 2021

 ECS COLLABORATION TOOL

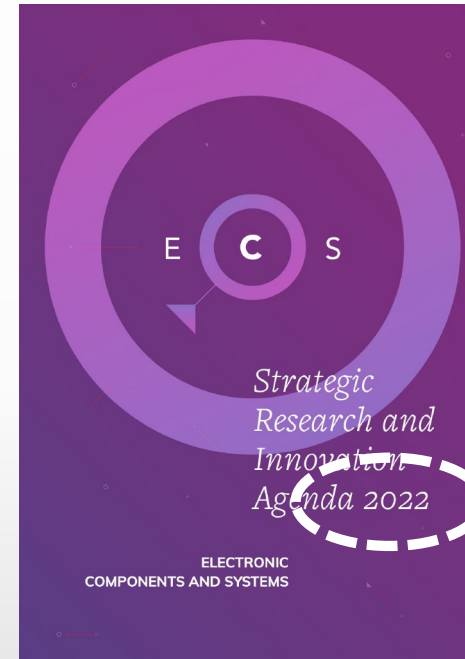


<https://ecscollaborationtool.eu/news-overview/news-ecs-sria-final.html>



**KDT-SRIA 2021**

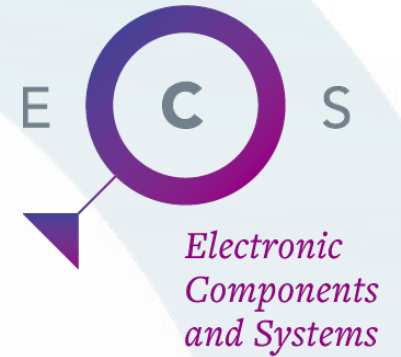
**KDT Call 1 (16/12/2021)**



**KDT-SRIA 2022**

**KDT Call 2 (05/2022)**

- Edited in 2021
- Published in January 2022



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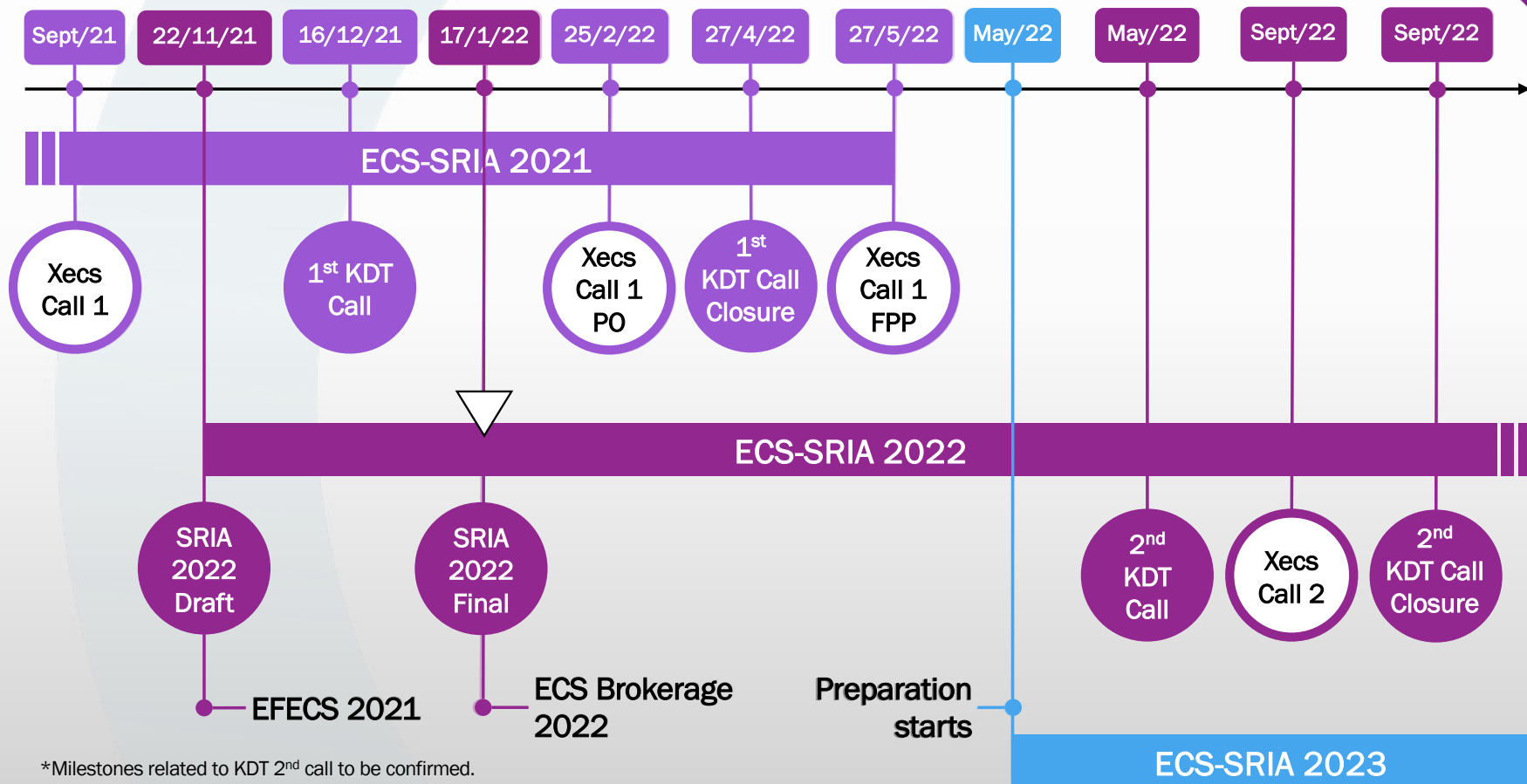


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# ECS-SRIA 2022 Timeline\*



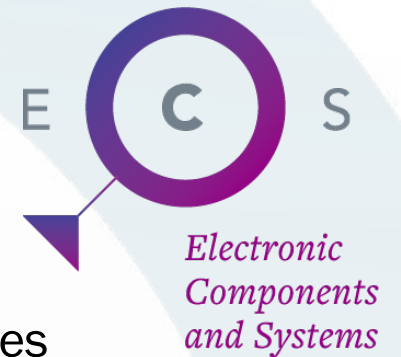
*Electronic Components and Systems*



\*Milestones related to KDT 2<sup>nd</sup> call to be confirmed.

# ECS-SRIA 2021: what is new?

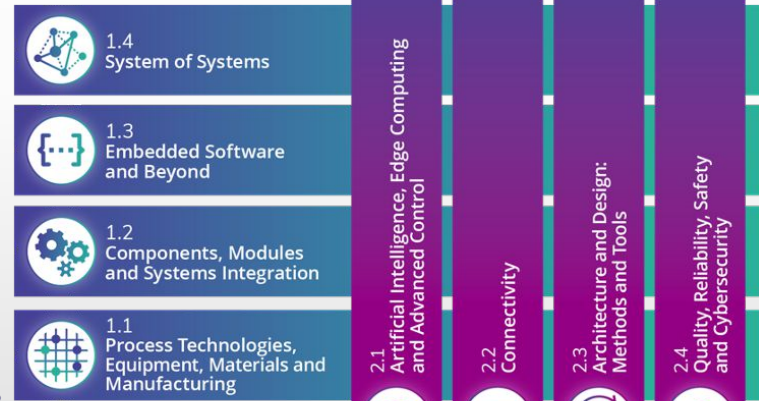
- ▶ New Structure
- ▶ Analysis of all Major Challenges allowed identification of 5 Main Objectives
- ▶ Global Timelines (short term, mid term and long term)
- ▶ Broadened Scope (Integrated photonics; flexible electronics topics; higher layers of software)
- ▶ New introduction, that guides the reader through the SRIA
- ▶ Updated Long Term Vision Chapter
- ▶ Common Glossary (SRIA definitions of specific terms)



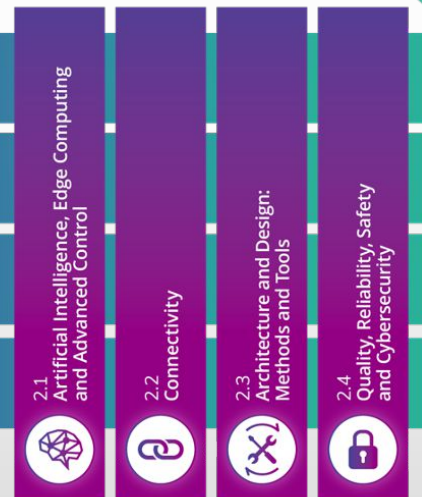
# ECS-SRIA 2021 structure



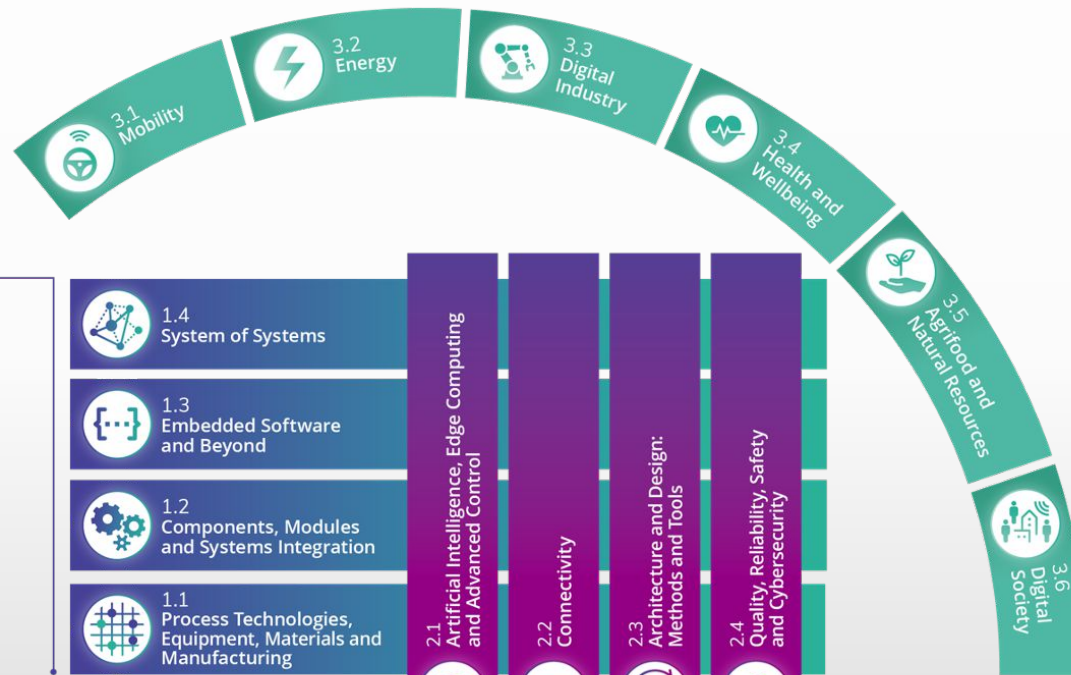
## 1 FOUNDATIONAL TECHNOLOGY LAYERS



## 2 CROSS-SECTIONAL TECHNOLOGIES



## 3 ECS KEY APPLICATION AREAS



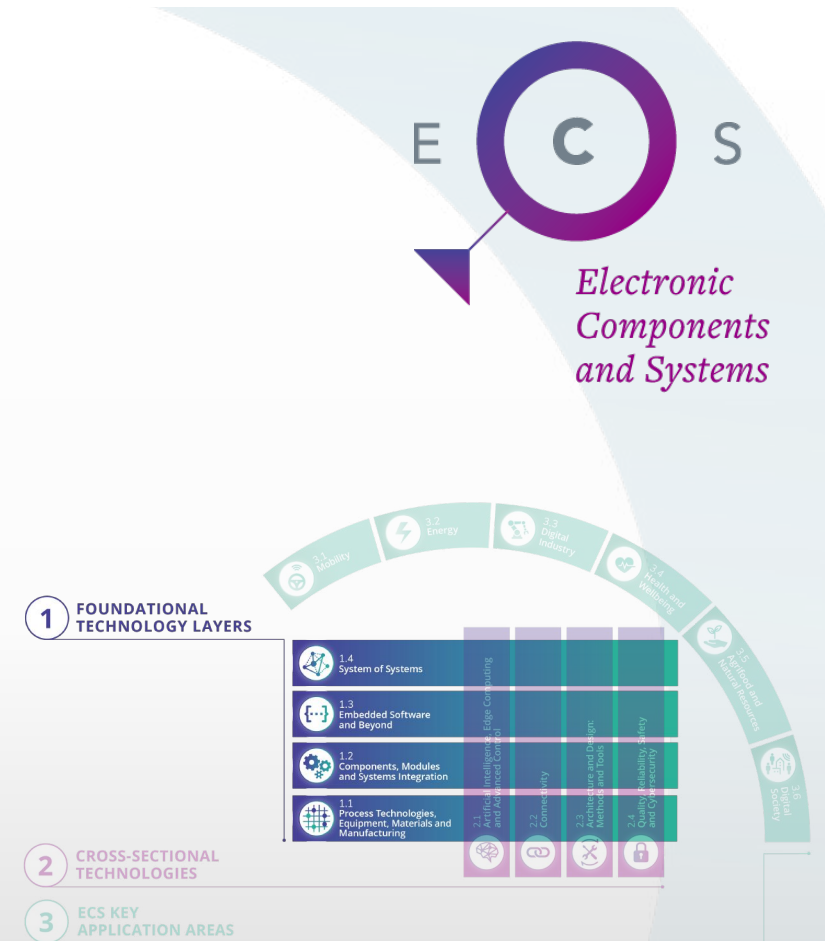
# Foundational technologies

The Foundational Technology Layers cover the technology stack of a typical digitalization solution based on ECS.

They have hierarchical dependencies, due to the inherent nature of ECS and the way they compose and integrate in complex entities.

Essential to creating the main components of a digitalization solution.

Represent a very fertile ground where new interdisciplinary technologies, products and solutions can grow.



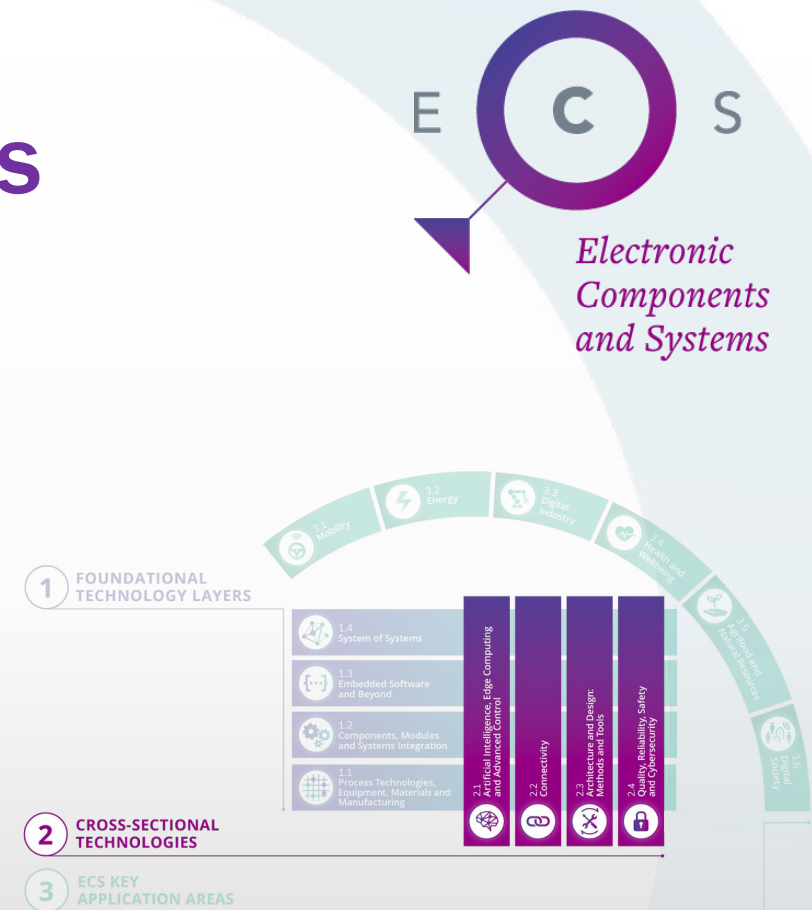
# Cross sectional technologies

Four Cross-Sectional Technology chapters focus on transversal areas, where innovative results emerge from the interdisciplinary contribution of the foundational layers.

E.g.: embedded intelligence on the edge requires

- new integrated circuits
- to develop innovative electronic components
- that can be used to develop smarter and more connected components, modules and entire systems,
- running smart software that will offer new functionalities and capabilities
- that will allow these systems to interact, cooperate and merge in larger Systems of Systems.

The innovation generated by cross-sectional technologies influences foundational layers and **amplifies the effect** of innovation also in the application domains.



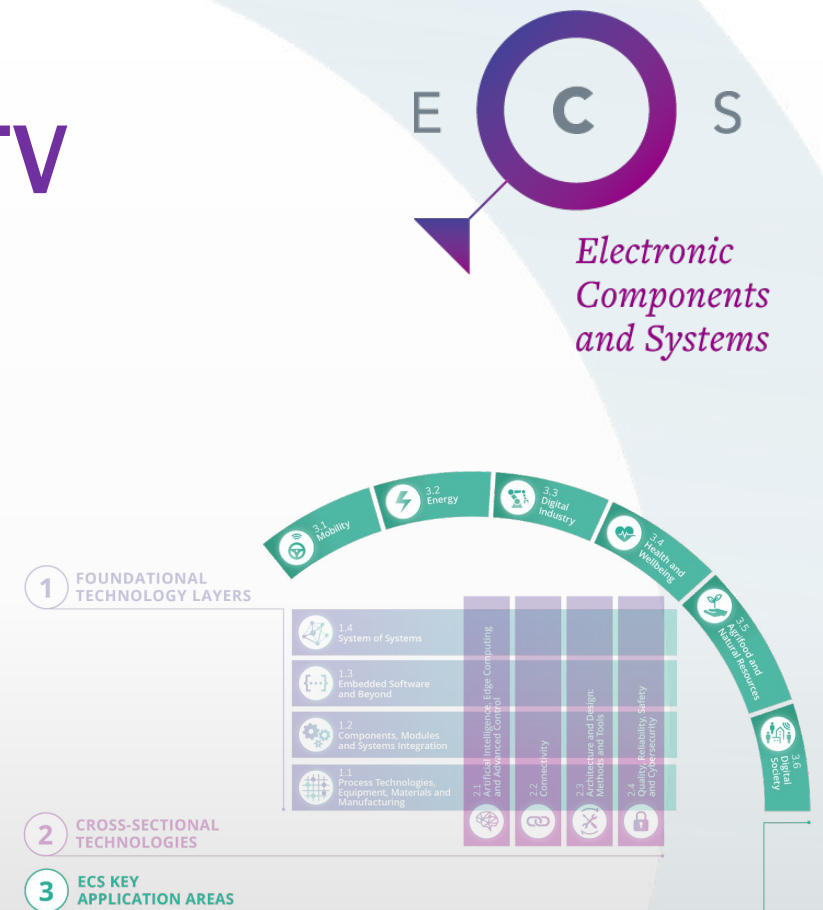


# Application chapters and LTV

Six Application chapters describe the challenges of specific ECS application domains, that are key for Europe, and identify the required R&D&I efforts.

Finally, the Long-Term Vision chapter illustrates our vision of the ECS beyond the time horizon covered by the other chapters:

- it seeks to identify the research subjects that must be addressed at low TRL levels
- and help the research programs in the continuous improvement of European digital technology



# EU Main Objectives covered by SRIA



*Electronic  
Components  
and Systems*

1



Boost industrial competitiveness through interdisciplinary technology innovations

2



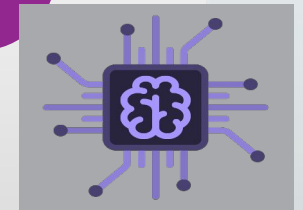
Ensure European digital autonomy through secure, safe and reliable ECS supporting key European application domains

3



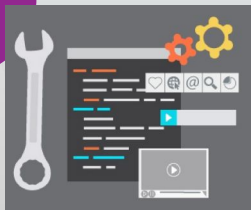
Establish and strengthen sustainable and resilient ECS value chains the Green Deal

4



Unleash the full potential of intelligent and autonomous ECS-based systems for the European Digital Age

X

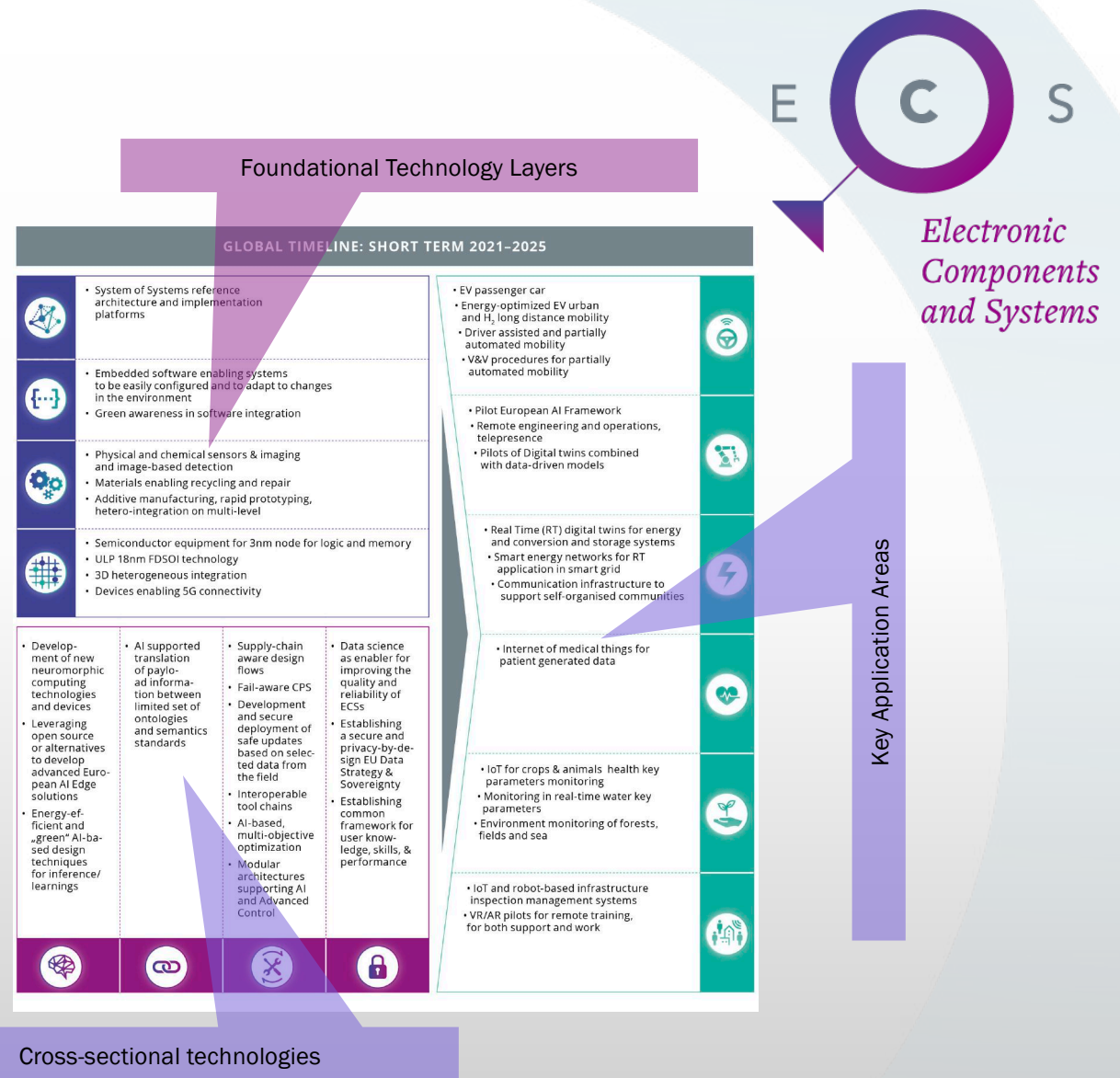


Ensure engineering support across the entire lifecycle of complex ECS-based systems

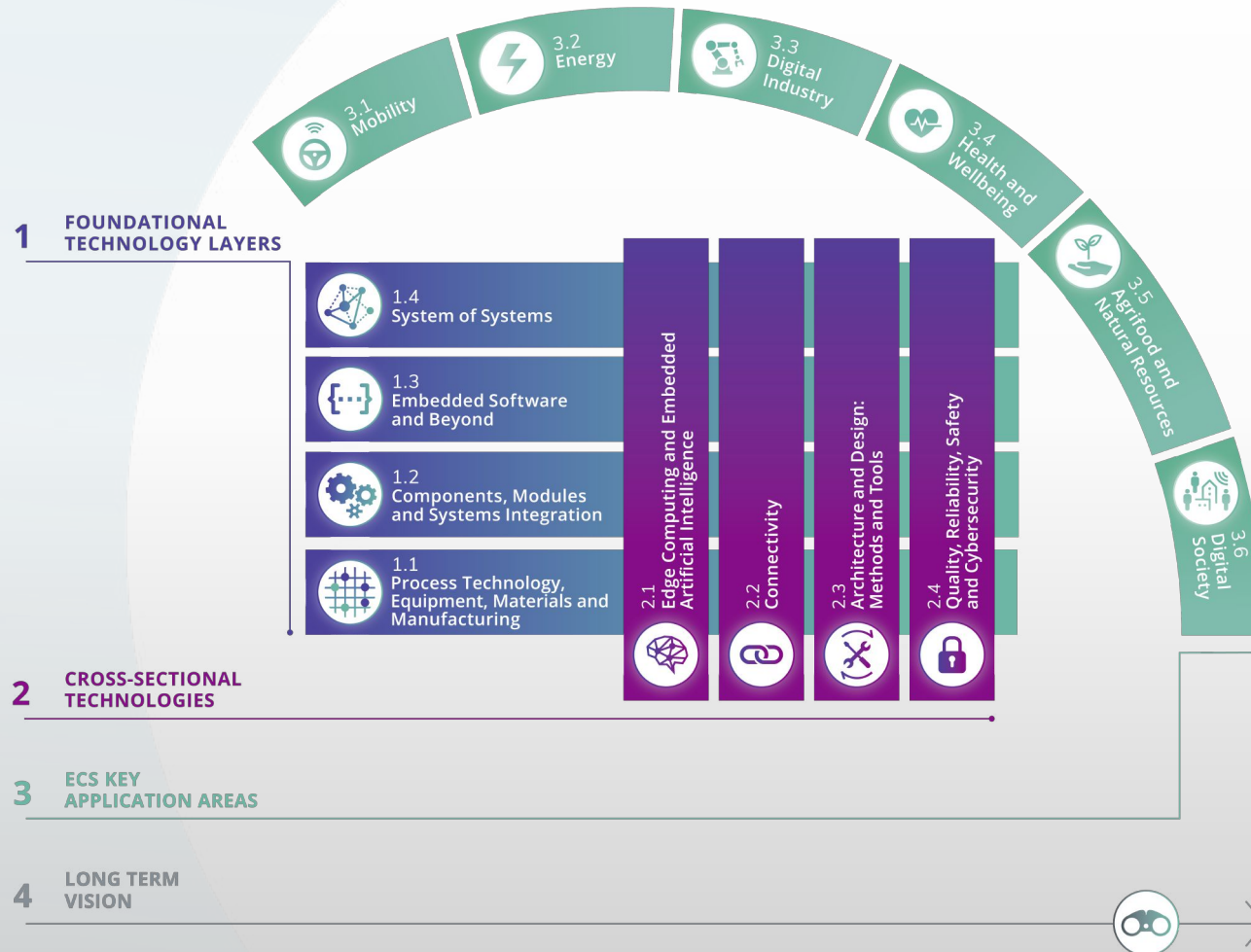
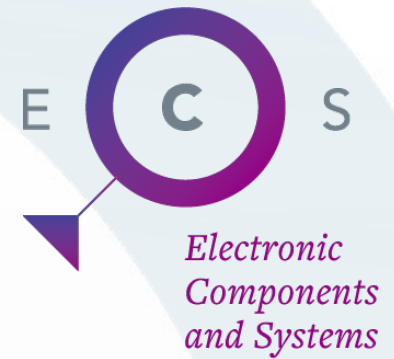
# Global Timelines

## Short-term example

- ▶ Global timelines provide a compact and structured view of the main milestones foreseen in the next 10 years.
- ▶ Three period:
  - ▶ Short term (2021–2025): The industry has a **precise idea** of what must be achieved during that timeframe.
  - ▶ Medium term (2026–2030): **Reasonably good knowledge** of what can possibly be achieved.
  - ▶ Long term (2031 and beyond): Expected achievements are more of a **prospective nature**.
- ▶ Described features expected to be available as ECS at TRL levels 8–9 (prototype or early commercialisation) within that timeframe
- ▶ Detailed timelines available in each technology or application section



# ECS-SRIA 2022 structure



# Objectives of 2022 Update

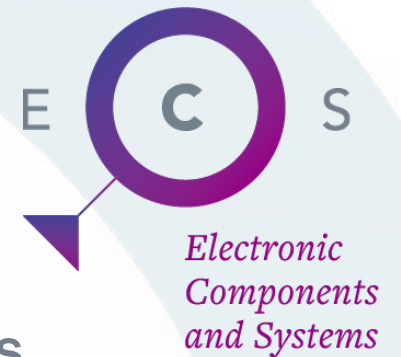
Updates follow/influence research and market trends and focus both on **contents** and on the **relations** between them.

Updates focused on **contents**:

- Improve the delineation of existing concepts and introduce new concepts
- Minimize unnecessary overlapping and avoid fragmentation

Updates are intended to highlight the **ECS-SRIA “systemic”** nature:

- Highlight and improve the **synergies** between the chapters
- Highlight **interdisciplinarity**
  - Between technology domains
  - Between technology and applications



# ECS SRA 2022 Outline



Electronic Components and Systems

**FOUNDATIONAL TECHNOLOGY LAYERS**

**1.4 - SYSTEM OF SYSTEMS**

**1.3 - EMBEDDED SOFTWARE AND SYSTEMS INTEGRATION**

**1.2 - COMPONENTS, MODULES AND EQUIPMENT MATERIALS AND MANUFACTURING**

**1.1 - PROCESS TECHNOLOGY AND MANUFACTURING**

| CROSS-SECTIONAL TECHNOLOGIES   |  |   |   |
|--|--|---|---|
| 2.1 - EDGE COMPUTING AND EMBEDDED ARTIFICIAL INTELLIGENCE  | 2.2 - CONNECTIVITY   | 2.3 - ARCHITECTURE AND DESIGN, METHODS AND TOOLS  | 2.4 - QUALITY, RELIABILITY, SAFETY AND CYBERSECURITY  |
| <p>Hardware architectures and their implementation (Systems of Systems, Embedded Architectures), for edge and hyper-edge devices. Cyber technologies for compute, storage and communication (generic embedded architectures and technologies that are more focused towards edge computing). Technologies for co-design using Artificial Intelligence at the edge.</p>  | <p>The connectivity and interoperability between devices is a key enabler for the projected commercial and societal benefits that are related to the CS model layers 1 to 6.</p>   | <p>Innovations, advancements and extensions in architectures, design processes and methods, and in computing tools and frameworks, that are enabling engineers to design and build innovative ECS-based applications with the desired quality properties, efficiency and cost-effectively.</p>  | <p>Ensure quality, reliability, safety, dependability, privacy and security of ECS as a part of the Design, Implementation and Validation Testing process of complex, heterogeneous and intelligent ECS, including human-systems interaction.</p>   |
| <p>System of Systems (SoS) enable the cooperation, orchestration, management, control and evolution of an entire system composed of embedded and cyber-physical systems (ECS). This layer covers SoS architecture, technologies to control and safety-critical ECS in SoS, ECS and SoS interoperability, advanced control and cyber security and interoperable SoS platforms, supported by SoS full lifecycle automated engineering.</p>   | <p>Artificial Intelligence to automatically manage the composition of ECS in SoS and control their evolution. Artificial Intelligence to improve/automate interoperability. Distributed artificial intelligence to provide the level of automation required to monitor, to support decision making and to control the complexity of SoS.</p>   | <p>Connectivity is a key enabler for SoS which, by definition, are composed of connected and distributed ECS. Connectivity channels and their interfaces are at the base of the composition process from which SoS originate.</p>   | <p>Engineering methodologies, tool chains and tools interoperability are fundamental to enable the definition of SoS architectures, the implementation of SoS platform and SoS management across the lifecycle. The heterogeneity of SoS requires automated engineering processes and toolchains, integrated between multiple stakeholders, brands and technologies, supporting efficiency, quality and sustainability.</p>   |
| <p>Facilitate engineering of embedded and cyber physical systems (ECS), enabling digitalization through the flexible and economically accessible building of larger software-enabled systems with better quality. This layer covers new applications of ECSs, continuous integration and deployment, ECSs engineering and management across their lifecycle, industry sustainability aspects. Starting from integrated hardware systems, this layer provides the embedded software (OS, libraries, virtualization middleware) required to produce fully functioning embedded and cyber physical systems.</p> | <p>Embedded software represents one of the key enablers of embedded intelligence. Embedding data analytics and artificial intelligence in devices allow to process data on the edge, take decisions on the edge, optimize operations, dynamically adapt and improve the cooperation between ECSs and sustainability. This layer provides also software support for AI-specific hardware, machine learning and federated intelligence on the edge.</p>  | <p>ECSs are, for the vast majority, connected and this layer provides them with all the elements required to ensure full connectivity, inter-system communications and the capability to interact with cloud platforms. These elements are key to enable the composition of ECSs in SoS, and also for the inclusion of legacy systems.</p>  | <p>Trust represents one the strongest barriers for the acceptance of ECS and it must be ensured in embedded software in particular for connected and future ECSs due to their complexity, require continuous hardware-software integration, both at component and system level. Continuous and automated engineering extends to ECSs deployment and to their entire lifecycle. These necessities increase when considering embedded AI and new computing paradigms (e.g. neuromorphic).</p>                             |
| <p>Multidisciplinary engineering for physical and functional heterogeneous integration of embedded functionalities involves physical entities at components, modules and system levels. Heterogeneous integration spans SoS, systems and products solutions. This layer involves hardware integrated systems including low level software (e.g. firmware and operating system drivers).</p>  | <p>Smart components, modules and systems are the hardware key enablers for the embedded intelligence. The focus is on integrating machine learning and artificial intelligence on the sensor, module and systems level. New, advanced, efficient and specialized processing architectures based on CPU, embedded GPU, accelerators, neuromorphic computing, FPGA and ASICs to increase the edge computing performance and reduce power consumption. Low level software support to enable AI-based data analysis is provided.</p> | <p>Connectivity solutions (communication modules &amp; interfaces) that are needed in networked embedded and cyber physical systems (ECS). Focus is on providing real-time, low-latency, low power for edge and IoT devices, protocols communication and electrical characteristics in small IoT packages. Focus cover also lifecycle engineering for optimized use materials, for components, modules and systems condition monitoring, predictive maintenance and to improve their recyclability.</p> | <p>Design and simulation methods that enable and support multi-physics and multi-modal design, simulation, manufacturing and testing must be addressed (e.g. modelling and design tools for thermal, mechanical and electrical characteristics in small IoT packages). Focus cover also lifecycle engineering for optimized use materials, for components, modules and systems condition monitoring, predictive maintenance and to improve their recyclability.</p>   |
| <p>Semiconductor process technology, equipment, materials and manufacturing form the base of the ECS value chain and, from single chip (e.g. SoC, more Moore) to more than Moore technologies (photonics, MEMS, Bio, etc.) and System on a Chip, they produce the chips (Packaged Single Chip, System in Package, Package on Chip and packaged device), the modules (e.g. Firmware and Single Chip, Packaged Device in Board) for all digital applications.</p>  | <p>AI adoption covers both the electronic components and their manufacturing process. Add intelligence close to the sensors (intelligence at the edge) and closer to the data sources (IoT) and integrate the components in a form factor that perfectly suits their applications. Use AI in the operation of semiconductor fabrication to master productivity, increase reliability, shorten time to market, yield, improve quality, productivity, sustainability, resource saving volume production of semiconductors.</p>     | <p>Electronic design and automation methods and tools required for ECS hyper-connectivity, including SoC/IC communications, advanced IP and protocols communication technologies to interface between sensors, actuators, essential and subsystems and systems.</p>   | <p>Electronic design and automation methods and tools required for ECS hyper-connectivity, including SoC/IC communications, advanced IP and protocols communication technologies to interface between sensors, actuators, essential and subsystems and systems.</p>   |
|  | <p>AI adoption covers both the electronic components and their manufacturing process. Add intelligence close to the sensors (intelligence at the edge) and closer to the data sources (IoT) and integrate the components in a form factor that perfectly suits their applications. Use AI in the operation of semiconductor fabrication to master productivity, increase reliability, shorten time to market, yield, improve quality, productivity, sustainability, resource saving volume production of semiconductors.</p>     | <p>Electronic design and automation methods and tools required for ECS hyper-connectivity, including SoC/IC communications, advanced IP and protocols communication technologies to interface between sensors, actuators, essential and subsystems and systems.</p>   | <p>End to end security starts from semiconductors. New technologies to address security at silicon level are considered, including application-specific, logic, heterogeneous SoC, security by design, etc. Quality and reliability of heterogeneous SoC production are also considered. Focus is on maximizing quality, market the process with AI, early detect reliability issues, qualify the parameters that influence HW reliability, adopt design for reliability, progress a health management of ECS, etc.</p> |

**KEY APPLICATION AREAS**

**3.1 - MOBILITY**

**3.2 - ENERGY**

**3.3 - DIGITAL INDUSTRY**

**3.4 - HEALTH AND WELLBEING**

**3.5 - AGRIFOOD AND NATURAL RESOURCES**

**3.6 - DIGITAL SOCIETY**

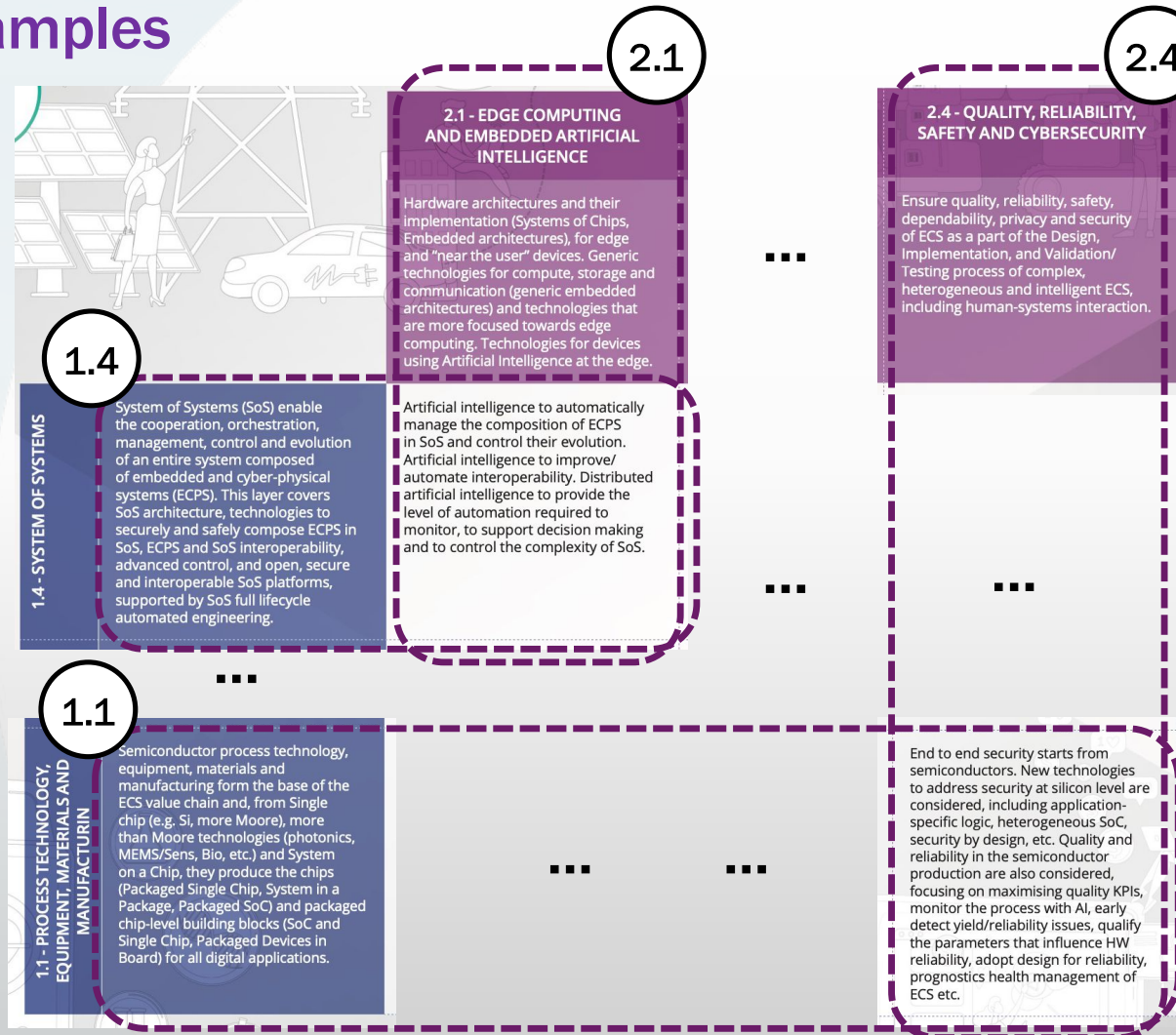
## LONG TERM VISION

The Long Term Vision chapter addresses research subjects to enable and support effective development of European industry in about a decade from today. The chapter could cover the challenges identified by the ECS SRA and specify long term industry needs. These needs are the basis for research programs for effective research and development in appropriate technological and/or application domains, so that European technological strength increases continuously in time and at the appropriate pace. Since last time from a first scientific breakthrough (T1) to the market presence of related products (TR10) is about 10 years, the effective identification of the future industrial needs is a determining factor for the success and speed of innovation. The Long Term Vision is shaped by three main factors: technology, application domains and policies. Clearly, all factors are drivers of innovation, because it anticipates technological advances lead to innovative applications of these advances and (3) user needs lead to technological innovations that enable these needs. At the same time, policies and politically established goals and processes lead technologies and applications towards common goals and targets such as the goals of the Green Deal and the European Industrial Competitiveness. It is apparent that, each of these factors motivates, shapes and initiates innovation efforts in many ways.

# Chapters synergies

## Some examples

E.g. AI in ECPS (2.1 MC 2, 3) and in SoS SW platforms (1.4 MC 1) are adopted to manage composition of ECPS in SoS, improve interoperability, etc.

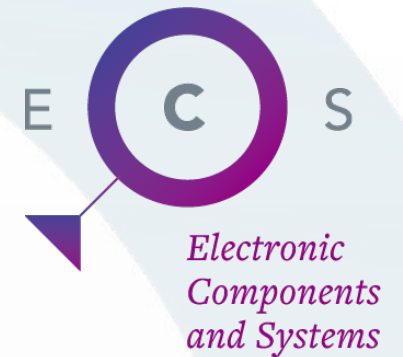


E.g. semiconductors (1.1 MC 2, 3) represent the basement of end-2-end security (2.4 MC 3)

E.g. quality and reliability in semiconductors production (2.4 MC 1), to maximise the yield, improve efficiency, etc. (1.1 MC 3, 4)

# Chapter cross references

To highlight the synergies between Chapters and provide hints to the reader, cross-references have been introduced alongside the text.



**PAGE MAIN TEXT**

ECS-SRIA 2022 — Strategic Research and Innovation Agenda for Electronic Components and Systems

**Lifecycle-aware holistic design flows**  
"Closing the loop" – i.e. collecting relevant data in the operation phase, analysing it (using AI-based or other methods) and feeding it back into the development phase (using digital twins, for example) – is the focus of this research topic. It is closely related to the major challenges "Continuous integration and deployment" and "Lifecycle management" in Chapter 1.3, which examines the software part of ECS, and Major Challenges 1 and 2 in Chapter 2.4.

Closing the loop includes data collected during operation of the system on all levels of the hierarchy, from new forms of misuse and cyber-attacks or previously unknown use cases and scenarios at the system level, to malfunctions or erroneous behaviour of individual components or modules. Analysing this data leads to design optimisations and development of updates, eliminating such errors or implementing extended functionality to cover "unknowns" and "incidents".

Data on physical aspects of the ECS must also be collected and analysed. This includes design for optimised manufacturing and deployment, awareness of physical effects and interferences, consideration of end-of-life (EOL) of a product and recycling options within a circular economy.

All of these aspects must be supported by new approaches for multi-level modelling, analysis, verification and formalisation of ECS's operational reliability and service life (c.f. previous challenges), including a consequent

**CROSS-REFERENCES**

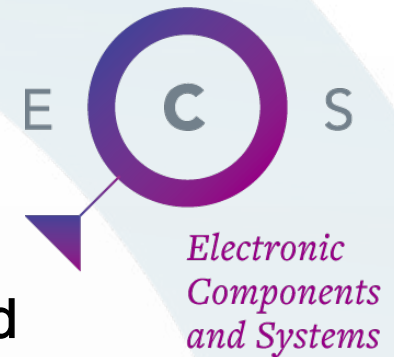
1.3 2.4

- Chapter icon
- Chapter number

Cross-references indicate that the topic described in the main text is linked to the referenced Chapter.



# ECS-SRIA 2022 updates



**SRIA 2022 updates cover, in different ways, the entire SRIA and include:**

- Feedback from the ECS community and the EU Commission on specific topics
- The input provided by the 6 thematic workshops
- Updates already planned last year
- Updates emerging this year

**New chapter leaders, e.g. in chapters 1.1 (PTEMM), 3.3 (Digital Industry) and 4 (LTV).**

**New contributors, in almost all the chapters.**

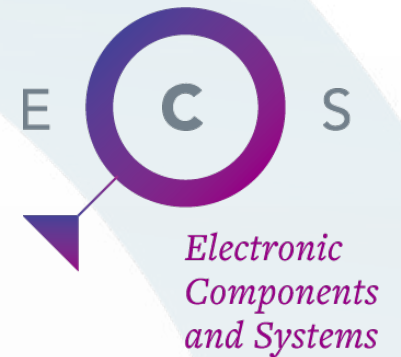
# ECS-SRIA 2022 updates (2)

## Introduction updates:

- Main Objectives update
  - Extension of the analysis to the new challenges and re-check of updated challenges
  - Main Objectives confirmation
- Global timelines update
- ECS-SRIA Outline

**Scope extension** to include quantum technologies, integrated photonics, flexible electronics and open-source hardware.

New **“Keywords Index”**, to quickly search key topics and simplify the SRIA “navigation” jumping directly to them.



## A

|                                     |     |
|-------------------------------------|-----|
| abstraction                         | 105 |
| accelerators                        | 465 |
| access control as a service (ACaaS) | 442 |
| actuating                           | 44  |

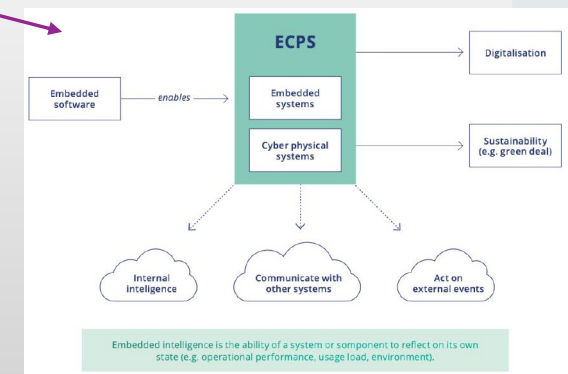
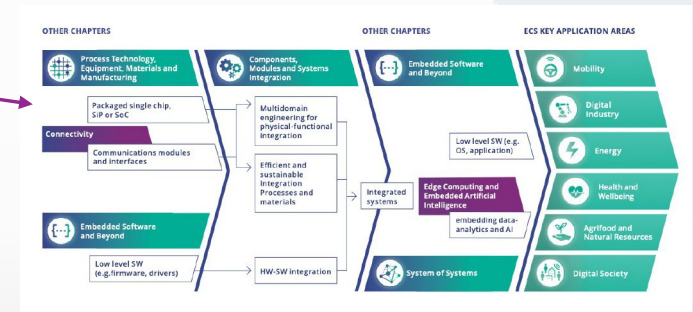
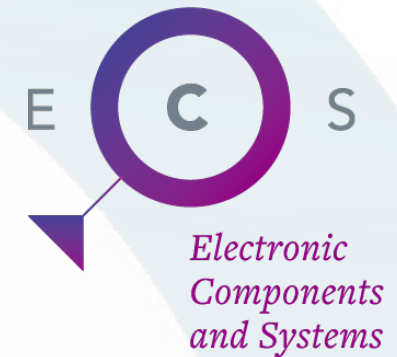
# ECS-SRIA 2022 updates (3)

## Chapters 1.1 and 1.2 (- Process Technology, Equipment, Materials and Manufacturing ):

- Improved delineation of concepts and synergies between the Chapters
  - SoC to System-in-Package (SiP) represents the transition between 1.1 and 1.2
  - In chapter 1.2, a new chapter structure has been included
- Extended focus on heterogeneous integration of devices and components for physical and functional integration (PFI) (1.2)
  - Including support for flexible electronics and photonics solutions

## Chapter 1.3 (Embedded Software and Beyond):

- Better delineation of the concept of Embedded and Cyber-physical System (ECPS).
- Stronger link with embedded intelligence (2.1)
- Trade off between HW resources and SW abstraction (Green Deal)
- More focus on:
  - Open-source software
  - Digital twin
  - SW features supporting SoS



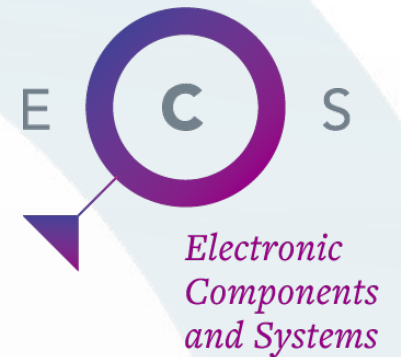
# ECS-SRIA 2022 updates (4)

## Chapter 1.4 (System of Systems):

- General restructuring and improvement of concepts delineation
- M.C. 1 and M.C. 5 (2021) merged in a new M.C. 1 - “SoS architecture and open integration platforms”
- “Advanced control” topic moved from Chapter 2.1 and created a new challenge M.C. 5 - “Major Challenge 5: control in SoS composed of embedded and cyber-physical systems”
- New M.C. 6 - “SoS monitoring and management”

## Chapter 2.1 (Edge Computing and Embedded Artificial Intelligence):

- Complete restructuring and editing
  - Improved delineation of Edge Computing and Artificial Intelligence
  - And their **convergence towards the embedded intelligence on the edge - Edge AI**
  - Classification of edge computing levels included
  - Positioning of Embedded Artificial Intelligence
  - All 4 M.C. split between Edge Computing and Embedded Intelligence
- Broaden the scope of “advanced control” that has been moved in chapter 1.4 (SoS):
  - Edge AI remains linked to advanced control as an enabler



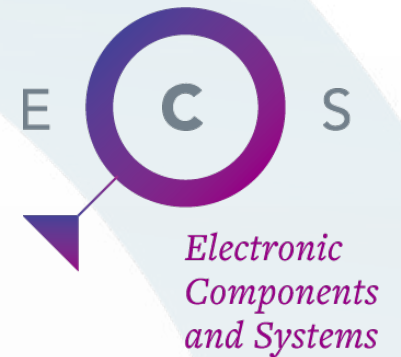
# ECS-SRIA 2022 updates (5)

## Chapter 2.2 (Connectivity):

- Analysis of European HW production capability (6G focus)
- 6G focus: alignment with 6G EU Initiatives
- Expansion of connectivity from point-to-point to application-to-application:
  - To support SoS paradigm and network virtualization
  - New M.C. 5: network virtualization enabling run-time engineering, deployment and management of edge and cloud network architectures.

## Chapter 2.3 (Architecture and Design: Methods and Tools):

- Better delineation and extended focus on:
  - Support for Fog-Edge-Cloud continuum
  - Integration platforms
  - Full lifecycle support, including maintenance and End-of-Life / second life aspects (Green Deal)
  - Support for AI based components
  - Support for legacy components
  - Support for (SW-)updates



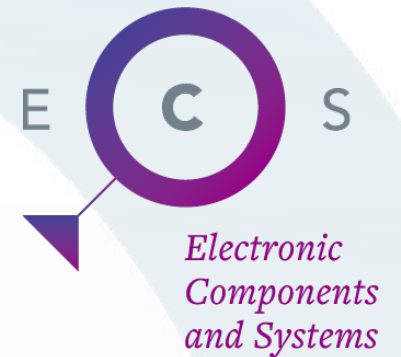
# ECS-SRIA 2022 updates (6)

## Chapter 2.4 (Quality, Reliability, Safety and Cybersecurity):

- New topics:
  - HW quality and reliability:
    - Digital twin – deeper look on the concept
    - Virtualization support
    - Simulation data and process management
    - Design to field – to improve test and modelling using field load simulator
    - SW/HW reliability in their interaction
  - Development of novel security and safety approaches with respect to energy and the impact on environment
- M.C. 5 updated from “Human Systems Interaction” to “Human Systems Integration”

## Application Chapters

Chapters 3.2, 3.3, 3.5, 3.6: general refresh, following the overall update guidelines



# ECS-SRIA 2022 updates (7)

## Chapter 3.1 (Mobility):

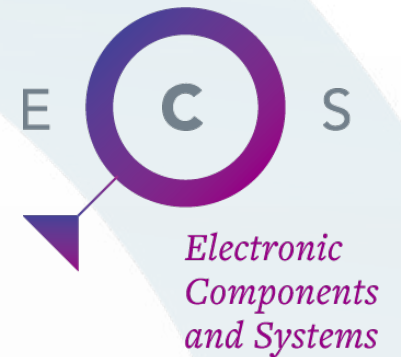
- New/updated topics:
  - SW defined vehicle
  - Importance of new HW and SW architectures in electronics for mobility
  - Edge2cloud continuum in mobility
  - Influence of pandemic on long-term vision

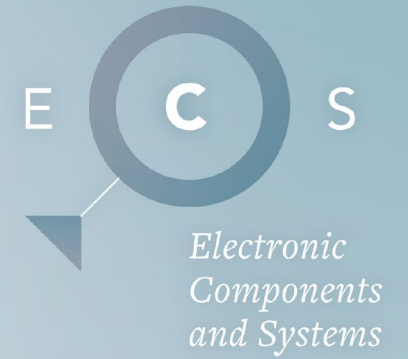
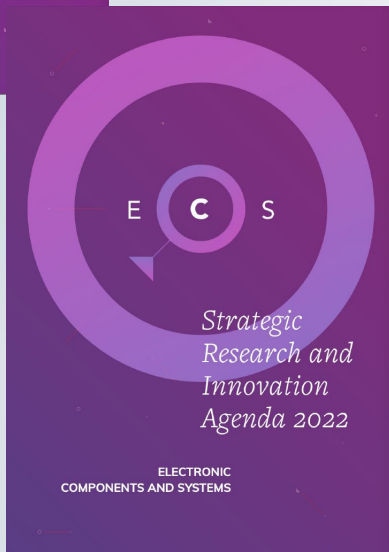
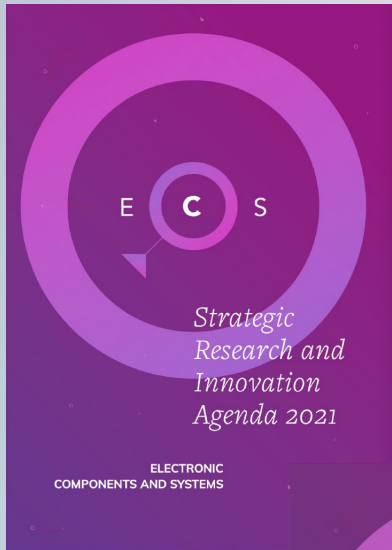
## Chapter 3.4 (Health and wellbeing):

- Refreshed the role of Integrated Silicon Photonics and Flexible Electronics
- Alignment with Health.E lighthouse

## Chapter 4 (LTV):

- Complete restructuring and re-editing of the ECS long-term vision
- All the SRIA Chapters have been included





# ECS-SRIA 2021 & 2022

Thanks for the attention.  
Any question?