

ECSEL-Austria

May 4, 2017
Bologna

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About ECSEL-Austria



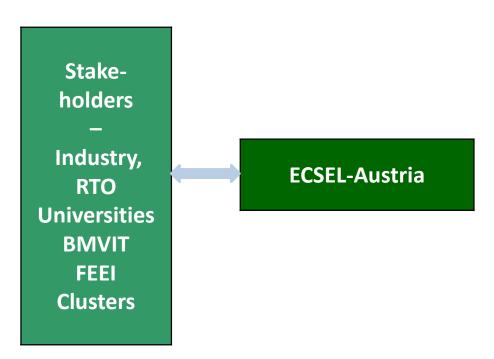
ECSEL-Austria is an industry-driven national research, development and innovation platform representing the technology areas of micro- and nanoelectronics, embedded systems and systems integration.

- 2013: founded based on ARTEMIS-Austria (embedded software and software architecture)
 and ENIAC-Austria (micro- and nanoelectronics)
- **2014:** enlarged by the areas of "systems and systems architecture", "framework conditions and visibility" and "network and network development" in 2014.
- Legal Status: Verein
- Members: 40 / 4/2017

ECSEL-Austria: the Austrian technology platform for Electronic based System



Secure supply of key technologies and critical knowhow in the field of electronic components and systems to support innovation in all major sectors of the economy and society in Europe.

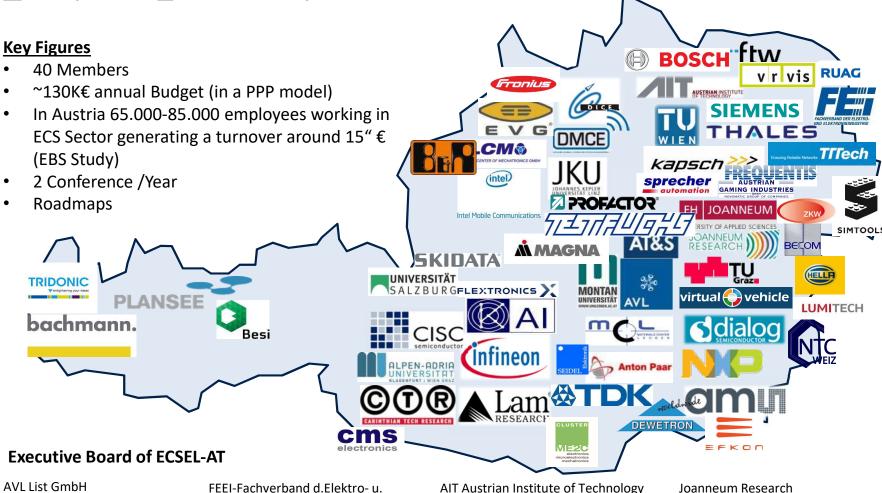


- Foster collaboration in the electronics components and systems domain (ECS)
- strengthen the international competitiveness of Austrian companies
- Represent national ECS community
- Support or run the development of related roadmaps
- Organize stakeholder events
- develop inputs for European research activities based on the strengths of Austrian actors

ECSEL Austria **Austrian Landscape** Legende Unternehmen ▲ F&E Organisationen Forschungbereiche Sensorik Material Hochfrequenztechnik und RFID Leistungselektronik und Elektronik Embedded system

Study: electronic based systems in Austria bmvit

<u>Electronic Components and Systems for European Leadership – ECSEL- Austria</u>



AVL List GmbH
[Dr. Josef AFFENZELLER]

FEEI-Fachverband d.Elektro- u. Elektronikindustrie [DI Dr. Klaus BERNHARDT]

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[Dipl.Ing Ewald WACHMANN]

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[Univ.-Prof. DI Dr. Wolfgang PRIBYL]

Infineon Technologies Austria [DI Johann MASSONER]

EPCOS OHG
[Dr. Georg KÜGERL]

AT&S
[Dr. Hannes VORABERGER]

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ECSEL-Austria

Starting Structure until 2016



Electronic Components and Systems for European Leadership ECSEL Austria

☐ embedded SW and SW Architectures (ARTEMIS)

Nanoelectronics (ENIAC) Systems and Architectures (EPoSS)

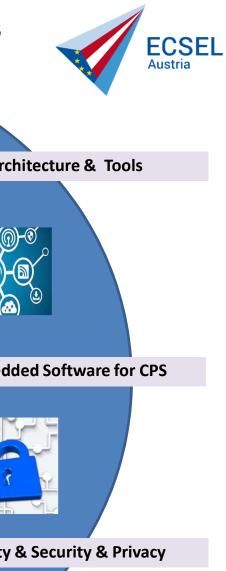
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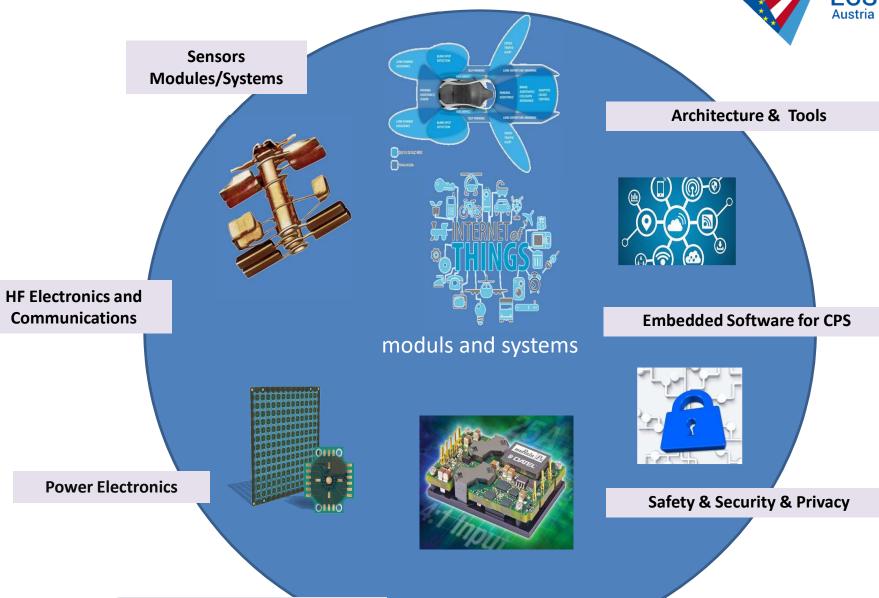
Roadmap, Network and Visibility

Common Roadmaps including adjustment with European and regional level, Network Framework Conditions and Visibility

ENABLING FRAMEWORK

ECSEL Austria: Stakeholder oriented selection of priorities

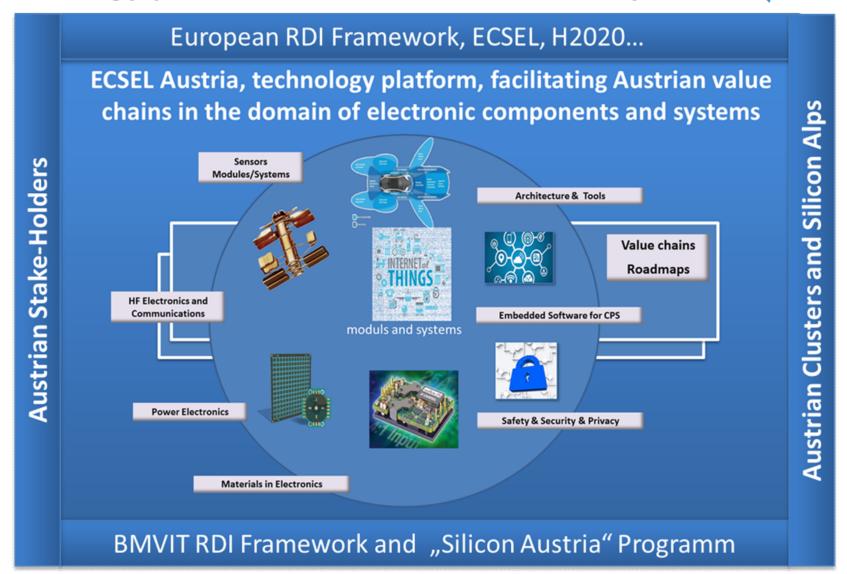




Materials in Electronics

Positioning of ECSEL-Austria as the Austrian technology platform for Electronic based System





Sensors Modules/Systems

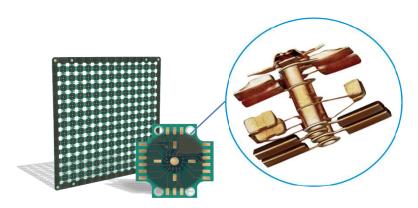
Highly integrated sensor systems – you do not see them, feel them, smell them, but they are there to generate data. They are operating in a **self – sufficient** way and just forward the information **securely** that they are generating. **Miniaturization** and ideal 3 dimensional packages are keen.





Materials in Electronics

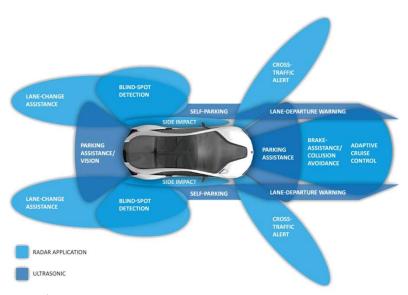
Ideal combination of different materials used for electronic components are challenging further miniaturization of electronics and/or are influencing reliability. Decent knowledge about material and material combination, surfaces and interfaces are keen to proceed development in the upcoming years. Current standards and solutions have to be challenged and redefined.





Power Electronics

Energy consumption of electronic devices are currently limiting their use. New **high energy efficient electronic modules** with **high power density** are required for devices in vehicles, energy supply, etc. in future





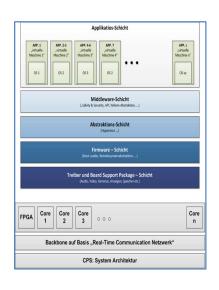
High Frequency Electronics and Communications High frequency communication for "Smart Systems" including low power, high frequency communication, RFID,

dependable real-time



Embedded Software for CPS

Distributed **stringent real-time** applications (high computing **complexity**, dependability, **low latency**, **ultra low power** applications, **distributed** software systems, usability, efficient **control** algorithms, sensor **fusion**)





Architecture and tools

Frameworks (SW components for communication, security, HW abstraction, multicore support), **Verification** and **validation** tools, **Smart system** development methods, adhoc **dynamic** SW **reconfiguration**



Safety, Security and Privacy
Dependability, safety and security HW
components and SW frameworks, functional
safety, privacy support, fail operational
systems,

